

**AMENDMENT & CLAIMS LISTING**

Please amend the claims as follows:

Claim 1 (cancelled)  
Claim 2 (cancelled)  
Claim 3 (cancelled)  
Claim 4 (cancelled)  
Claim 5 (cancelled)  
Claim 6 (cancelled)  
Claim 7 (cancelled)  
Claim 8 (cancelled)  
Claim 9 (cancelled)  
Claim 10 (cancelled)  
Claim 11 (cancelled)  
Claim 12 (cancelled)  
Claim 13 (cancelled)  
Claim 14 (cancelled)  
Claim 15 (cancelled)  
Claim 16 (cancelled)  
Claim 17 (cancelled)  
Claim 18 (cancelled)  
Claim 19 (cancelled)  
Claim 20 (cancelled)  
Claim 22 (cancelled)  
Claim 23 (cancelled)  
Claim 24 (cancelled)  
Claim 25 (cancelled)

Claim 26 (cancelled)  
Claim 27 (cancelled)  
Claim 28 (cancelled)  
Claim 29 (cancelled)  
Claim 30 (cancelled)  
Claim 31 (cancelled)  
Claim 32 (cancelled)  
Claim 33 (cancelled)  
Claim 34 (cancelled)  
Claim 35 (cancelled)  
Claim 36 (cancelled)  
Claim 37 (cancelled)  
Claim 38 (cancelled)  
Claim 39 (cancelled)  
Claim 40 (cancelled)  
Claim 41 (cancelled)  
Claim 42 (cancelled)  
Claim 43 (cancelled)  
Claim 44 (cancelled)  
Claim 45 (cancelled)  
Claim 46 (cancelled)  
Claim 47 (cancelled)  
Claim 48 (cancelled)  
Claim 49 (cancelled)  
Claim 50 (cancelled)  
Claim 51 (cancelled)  
Claim 52 (cancelled)  
Claim 53 (cancelled)  
Claim 54 (cancelled)

Claim 55 (currently amended). A semiconductor light source for emitting light to illuminate a space used by humans, the semiconductor light source comprising:

- an enclosure, said enclosure being fabricated from a material substantially transparent to white light,
- a base to which said enclosure is mounted,
- an interior volume within said enclosure,
- a secondary heat sink located in said interior volume, said secondary heat sink being capable of drawing heat from one or more semiconductor devices,
  - a plurality of primary heat sinks mounted on said secondary heat sink, each of said primary heat sinks being smaller than said secondary heat sink,
  - a semiconductor chip capable of emitting light mounted on one of said primary heat sinks, said semiconductor chip being capable of emitting monochromatic light, said semiconductor chip being selected from the group consisting of light emitting diodes, light emitting diode arrays, laser chips, and VCSEL chips,
    - said chip including a substrate on which epitaxial layers are grown,
    - a buffer layer located on said substrate, said buffer layer serving to mitigate differences in material properties between said substrate and other epitaxial layers,
    - a first cladding layer serving to confine electron movement within the chip, said first cladding layer being adjacent said buffer layer,
    - an active layer, said active layer emitting light when electrons jump to a valance state,
    - a second cladding layer, said second cladding layer positioned so that said active layer lies between cladding layers,
    - a first and a second reflective layer, each of said first and second reflective layers being located on opposite sides of said active layer, said reflective layers serving to reflect light emitted by said active layer, said reflective layers including multiple quantum wells,
    - a contact layer on which an electron may be mounted for powering said chip, and
    - a coating for converting monochromatic light emitted by said chip to white light, and
    - a plurality of mounting panels located on said secondary heat sink, said primary heat sinks being mounted to said mounting panels on said secondary heat sink, said mounting panels on said secondary heat sink being arranged so that light emitted by semiconductor chips

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located on said primary heat sinks which are located on said mounting panels is dispersed in a desired direction.

Claim 56 (previously submitted). A device as recited in claim 55 wherein said substrate is selected from the group consisting of Si, GaAs, GaN, InP, sapphire, SiC, GaSb, InAs.

Claim 57 (previously submitted). A device as recited in claim 55 wherein said substrate is electrically conductive.

Claim 58 (previously submitted). A device as recited in claim 55 wherein said substrate is electrically insulative.

Claim 59 (previously submitted). A device as recited in claim 55 wherein at least one of said epitaxial layers includes a material selected from the group consisting of GaN, AlGa<sub>N</sub>, AlN, AlGa<sub>N</sub>, GaInN, and GaInN.

Claim 60 (previously submitted). A device as recited in claim 55 further comprising a phosphor coating on said chip.

Claim 61 (previously submitted). A device as recited in claim 55 further comprising a power module for powering the light source, said power module including a fitting for installation in a traditional light bulb socket and an AC/DC converter for converting AC power from traditional building wiring to DC power usable by a semiconductor devices.

Claim 62 (previously submitted). A device as recited in claim 55 wherein at least one of said heat sink includes a material selected from the group consisting of include copper, aluminum, silicon carbide, boron nitride natural diamond, monocrystalline diamond, polycrystalline diamond, polycrystalline diamond compacts, diamond deposited through chemical vapor deposition and diamond deposited through physical vapor deposition.

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Claim 63 (previously submitted). A device as recited in claim 55 further comprising a quantity of heat conductive adhesive located between said chip and said primary heat sink and serving to conduct heat from said chip to said primary heat sink.

Claim 64 (previously submitted). A device as recited in claim 55 further comprising a quantity of light reflective adhesive located between said chip and said primary heat sink.

Claim 65 (previously submitted). A device as recited in claim 55 wherein at least one of said reflective layers includes multiple quantum wells.

Claim 66 (currently amended). A semiconductor light source for emitting light to illuminate a space used by humans, the semiconductor light source comprising:

- an enclosure, said enclosure being fabricated from a material substantially transparent to white light,
- a base to which said enclosure is mounted,
- an interior volume within said enclosure,
- a secondary heat sink located in said interior volume, said secondary heat sink being capable of drawing heat from one or more semiconductor devices,
  - a plurality of primary heat sinks mounted on said secondary heat sink, each of said primary heat sinks being smaller than said secondary heat sink,
  - a plurality of wells, said wells being located on said primary heat sinks and being sized to accommodate mounting of a semiconductor chip therein,
  - a semiconductor chip capable of emitting light mounted in one of said primary heat sink wells, said semiconductor chip being capable of emitting monochromatic light,
  - a quantity of adhesive serving to secure said chip to said primary heat sink,
  - said chip including a substrate on which epitaxial layers are grown,
  - a buffer layer located on said substrate, said buffer layer serving to mitigate differences in material properties between said substrate and other epitaxial layers,
  - a first cladding layer serving to confine electron movement within the chip, said first

cladding layer being adjacent said buffer layer,

an active layer, said active layer emitting light when electrons jump to a valance state,  
a second cladding layer, said second cladding layer positioned so that said active layer lies between cladding layers, and

a contact layer,

a first and a second reflective layer, each of said first and second reflective layers being located on opposite sides of said active layer, said reflective layers serving to reflect light emitted by said active layer, said reflective layers including multiple quantum wells, and

a coating for converting monochromatic light emitted by said chip to white light;

a plurality of mounting panels located on said secondary heat sink, said primary heat sinks being mounted to said mounting panels on said secondary heat sink, said mounting panels on said secondary heat sink being arranged so that light emitted by semiconductor chips located on said primary heat sinks which are located on said mounting panels is dispersed in a desired direction,

an air passageway within said secondary heat sink, said air passageway having an air entry and an air exit, said air passageway permitting air to enter at said air entry, travel through said air passageway and depart through said air exit, in order to receive heat from said secondary heat sink and dissipate said heat into the atmosphere.

Claim 67 (previously submitted). A device as recited in claim 66 wherein said substrate is selected from the group consisting of Si, GaAs, GaN, InP, sapphire, SiC, GaSb, InAs.

Claim 68 (previously submitted). A device as recited in claim 66 wherein said substrate is electrically conductive.

Claim 69 (previously submitted). A device as recited in claim 66 wherein said substrate is electrically insulative.

Claim 70 (previously submitted). A device as recited in claim 66 wherein at least one of said epitaxial layers includes a material selected from the group consisting of GaN, AlGaIn, AlN,

AlGa<sub>x</sub>N<sub>1-x</sub>, GaInN<sub>2</sub>, and GaInN.

Claim 71 (previously submitted). A device as recited in claim 66 further comprising a phosphor coating on said chip.

Claim 72 (previously submitted). A device as recited in claim 66 further comprising a power module for powering the light source, said power module including a fitting for installation in a traditional light bulb socket and an AC/DC converter for converting AC power from traditional building wiring to DC power usable by a semiconductor devices.

Claim 73 (previously submitted). A device as recited in claim 66 wherein at least one of said heat sink includes a material selected from the group consisting of include copper, aluminum, silicon carbide, boron nitride natural diamond, monocrystalline diamond, polycrystalline diamond, polycrystalline diamond compacts, diamond deposited through chemical vapor deposition and diamond deposited through physical vapor deposition.

Claim 74 (previously submitted). A device as recited in claim 66 further comprising a quantity of heat conductive adhesive located between said chip and said primary heat sink and serving to conduct heat from said chip to said primary heat sink.

Claim 75 (previously submitted). A device as recited in claim 66 further comprising a quantity of light reflective adhesive located between said chip and said primary heat sink.

Claim 76 (previously submitted). A device as recited in claim 66 further comprising a cover over said chip in said well.

Claim 77 (previously submitted). A device as recited in claim 66 further comprising a coating over said chip in said well.

Claim 78 (previously submitted). A device as recited in claim 66 further comprising a transparent material located in said well and covering said chip, said transparent material being